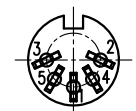
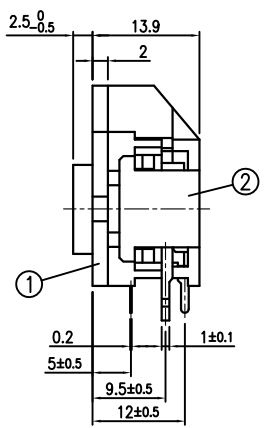
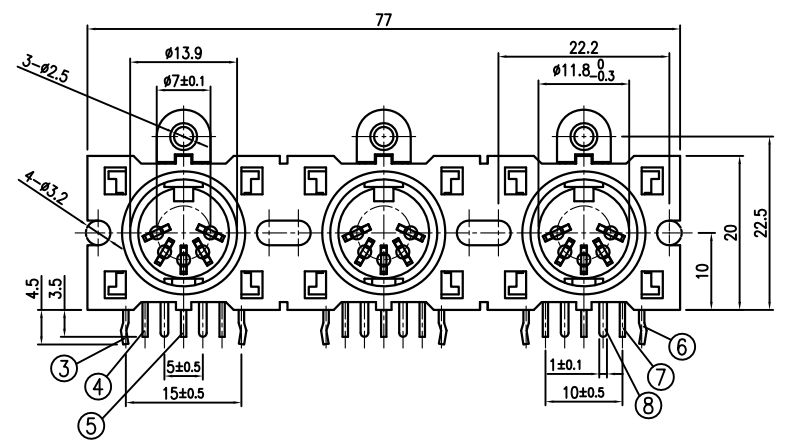
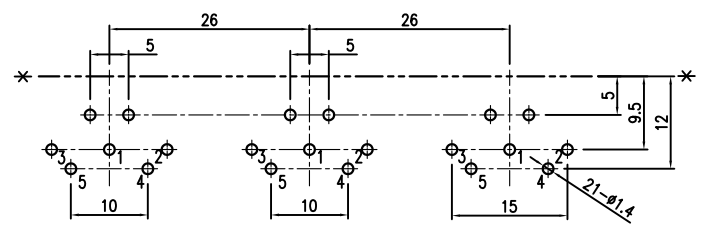


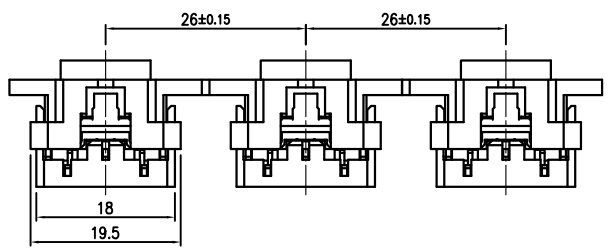
REVISIONS					
LTR	DESCRIPTION	DATE	REV.	CHKD.	APVD.
△x0	To revise the drawing by NO.F06160936	2016.04.07	陳衛兵	張啟濤	陳宗碧



PIN LAYOUT TO INSERT PLUG



PCB Layout Dimension (Tolerance: ±0.10)  
(Recommend min. Dimension)(Bottom View)



8	TIP SPRING	3	PHOSPHOR	t=0.20	MBCu2-Sn3
7	TERMINAL	3	BRASS	t=0.70	MBCu2-Sn3
6	TERMINAL	3	BRASS	t=0.70	MBCu2-Sn3
5	TERMINAL	3	BRASS	t=0.70	MBCu2-Sn3
4	TERMINAL	3	BRASS	t=0.70	MBCu2-Sn3
3	TERMINAL	3	BRASS	t=0.70	MBCu2-Sn3
2	COVER	3	PPO	UL 94V-1	BLACK
1	HOUSING	1	PPO	UL 94V-1	BLACK

LTR	PART NAME	Q'TY	MATERIAL	REMARK
0°	±2°	DWN	李志堅	DATE 2008.05.24
0	±0.5	DSND	夏正雄	DATE 1998.04.17
0.0	±0.3	CHKD	廖秋妹	DATE 2008.05.24
0.00	±0.1	APVD	夏正雄	DATE 2008.05.28
DIMENSION TOLERANCE		KUNMING ELECTRONICS CO.,LTD.		
				DWN.NO. A-052AP-03
				1/1 B

The raw material specified by Sony should meet SS-00259 requirement of the latest version. When such material is used, it should be supplied by Sony designated suppliers.